

VERTICAL-CAVITY SURFACE-EMITTING PHASE SHIFTER

DHAIFALLAH ALMUTAIRI^{1,2}, KARL JOHNSON¹, ALEXEI SMOLYANINOV¹,
ANDREW GRIECO³, YESHAIAHU FAINMAN¹

1 Department of Electrical and Computer Engineering, University of California, San Diego, California 92093, USA.

2 King Abdulaziz City for Science and Technology (KACST), Riyadh cisc11442, Saudi Arabia.

3 Qualcomm Institute, University of California, San Diego, California 92093, USA.

Daalmuta@eng.ucsd.edu

Abstract: This study proposes a novel technique for a 2D beam steering system using hybrid plasmonic phase shifters with a cylindrical configuration in a 2D periodic array suitable for LIDAR applications. A nanoscale VCSEP design facilitates a sub-wavelength spacing between individual phase shifters, yielding an expanded field of view and side lobes suppression. The proposed design includes a highly doped sub-micron silicon pillar covered by a thin layer of nonlinear material and an additional conductive metal layer. Characterization of a single VCSEP demonstrated a Free Spectral Range (FSR) of 53.28 ± 2.5 nm and a transmission variation of 3dB, with $V_{\pi}L$ equal to 0.075 V-mm.

Keywords: LIDAR, Phase shifter, Two-dimensional beam steering, Hybrid plasmonic waveguide.

1. Introduction

Optical beam steering is an important technology utilized by many applications, including optical wireless communication systems[1], LiDAR systems[2], remote sensing[3], and medical imaging[4]. Phased arrays represent the most advanced beam steering technique, widely used in radio frequency (RF) systems. The primary drawback of this approach is the unfavorable manner in which the emitter array spacing scales with wavelength. This poses formidable fabrication challenges at optical and infrared frequencies. For example, achieving a 110° steering angle for a 1550 nm wavelength beam requires an array period of around $0.5 \mu\text{m}^2$ [5,6].

State-of-the-art chip-scale phased array optics have been implemented using 2D phase shifter arrays based on waveguide-coupled directional gratings[7]. These devices employ an architecture where photonic waveguides supply a linear array of add/drop filters. The coupling into each add/drop filter can be modulated thermally using metal heaters. Following the add/drop process, light is transmitted to grating nanoantennas, which emit the light vertically. Compact 64×64 phase shifter arrays with a pixel size of $9 \mu\text{m} \times 9 \mu\text{m}$ or $89 \mu\text{m}^2$ have been reported[7]. However, such approaches are inherently limited in pixel size by the waveguide couplers themselves, which, due to the physical requirements of wavelength scaling, must be longer than $1 \mu\text{m}$, as observed in[7].

This paper presents an innovative method for designing optical phase shifters using hybrid plasmonic phase shifters in a cylindrical configuration. This approach holds significant potential for constructing large-scale 2D optical phase shifter arrays with sub-wavelength spacing. Furthermore, we provide an in-depth analysis of the proposed optical phaser shifter design, fabrication, and characterization.

2. APPROACH DESCRIPTION:

Our proposed approach involves the utilization of the hybrid plasmonic phase shifter in cylindrical geometry to construct a hybrid plasmonic resonator called a Vertical Cavity Surface

Emitting Plasmonic (VCSEP) device, shown schematically in Figure 1. The optical input beam is introduced via an integrated bottom layer into the VCSEP device, which modulates this optical carrier. The modulated field is re-emitted vertically through the top of the device. As the input light traverses the VCSEP device, it is converted into a localized hybrid plasmonic mode in a resonant configuration, modulated in the nonlinear optical material by the voltage applied across it, and subsequently emitted from the output of the device. The fusion of localization and resonance effects significantly augments the effective interaction length. A voltage is applied to the metal shell through a metal contact. At the same time, a doped silicon core connected to a doped silicon substrate serves as the ground electrode. This induces an electric field within the nonlinear electro-optic material inside the VCSEP, consequently modulating the refractive index within an individual VCSEP element. The synergy of enhanced effective interaction length and modulating refractive index within the cavity facilitates a phase shift. The light is then re-emitted into free space.

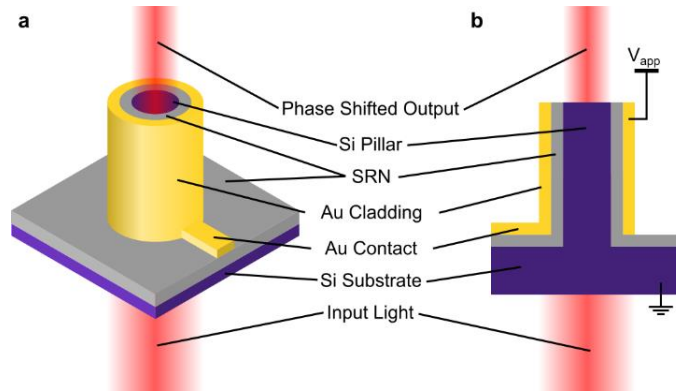


Fig. 1 A schematic diagram for the proposed Vertical Cavity Surface Emitting Phase shifter (VCSEP) illustrates its operating principle in the transmission mode. Light enters through the bottom of the device, undergoes a phase shift, and exits from the top side. (a) The VCSEP structure comprises a highly doped silicon core coated with a thin nonlinear dielectric core, specifically Silicon Rich Nitride (SRN), covered with a thin layer of gold. (b) A cross-section view of the VCSEP device provides a more detailed view of the device structure. This view highlights using gold as an electrical connection, allowing for applying an electrical field across the nonlinear material. The gold layer can be utilized for electrical connection to apply an electrical field across the nonlinear material. The substrate is utilized as a ground due to the high doping concentrations.

VCSEP devices exploit hybrid plasmonic localization to amplify the effective interaction length of light with the nonlinear material present within the resonant cavity. Due to plasmonic effects in the vertical architecture, the enhancement in interaction length results in more pronounced phase shifts. Reduced loss can be achieved while maintaining high localization due to the overlap between photonic and plasmonic modes. Moreover, the impedance mismatch between the vertical hybrid plasmonic waveguide and free space generates a low finesse, low-quality factor (Q) cavity resonator.

Utilizing a low- Q cavity mitigates the traditional bandwidth and spectral range constraints typically encountered in high- Q photonic resonator-based modulators while benefiting from an interaction length increase due to a cavity finesse ranging from 10 to 20. This effect is further amplified by large k -vector plasmonic modes that exhibit spatial localization: hybrid plasmonic modes excited by a free space 1550 nm laser will possess an effective wavelength up to 20 times shorter within the hybrid plasmonic structure[8,9].

3. DESIGN AND SIMULATIONS:

The versatility of the hybrid plasmonic design facilitates optimization for either low loss (less than 1000 dB/cm) or high localization, contingent on the radii of the Si core, the interstitial

layer of nonlinear dielectric, and the metal shell. In the case of a small Si core, the photonic mode is compelled into the nonlinear dielectric between the metal and Si core, resulting in enhanced interaction with the nonlinear dielectric while maintaining some plasmonic localization (as shown in the left panel of Figure 2). Alternatively, the electromagnetic field is effectively confined within the nonlinear dielectric with a thick Si core and substantial metal cladding. It exhibits strong localization in a thin section of the nonlinear dielectric at the cost of increased loss (as depicted in the right panel of Figure 2).

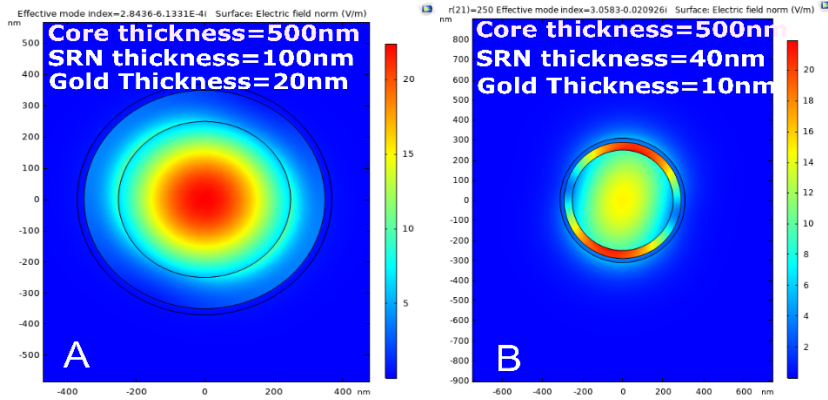


Fig. 2: Representation of two possible configurations for VCSEP and their corresponding normalized E-field localization. The first configuration, (a), illustrates the normalized E-field obtained from a Lumerical simulation where the core radius measures 250nm, coated with a 100 nm silicon Rich Nitride (SRN) finally sputtered with 20 nm of metal. The second configuration, (b), displays the normalized E-field obtained from a Lumerical simulation where the core radius is 250nm, coated with a 40 nm Silicon Rich Nitride (SRN), and finally sputtered with 10 nm of metal.

Following simulations in COMSOL, we produced two VCSEP designs. The first design is a lower loss (~ 1300 dB/cm) configuration (a) where the hybrid photonic/plasmonic mode is moderately localized by the gold shell but primarily pulls the photonic mode out of the high index Si core into the SRN dielectric layer. This configuration is optimized for the high nonlinear coefficient leveraging an extremely good mode overlap between the EO material and the photonic mode. A second configuration (b) relies on greater plasmonic localization at a higher loss (2300 dB/cm). This configuration has higher plasmonic localization for a material platform with weaker nonlinear coefficients. Although the linear loss coefficient is relatively high for both options, note that the length of the VCSEP is approximately $7\mu\text{m}$, resulting in calculated insertion losses of only 0.91dB and 1.61dB for configurations A and B, respectively. Silicon Rich Nitride (SRN) as a nonlinear material provides an optimal material since the material index often combines second and third-order nonlinear susceptibility contributions; this is due to the contribution of Pockets and DC-Kerr effects, respectively[10]. Moreover, SRN has lower optical loss and higher breaking voltage[11,12] The used SRN has been characterized to have $\chi(2)$ of 22.7 pm/V at a refractive index of 2.25[13].

4. FABRICATION PROCESS:

The sample substrate is a highly n-doped silicon wafer with a doping concentration of $4.5 \times 10^{18} \text{ cm}^{-3}$. The sample is first spin-coated utilizing polymethyl methacrylate (PMMA), achieving a thickness of 500 nm. Subsequently, a pattern was transferred to the sample via Vistec EBPG 5200 electron-beam lithography using a $1500 \mu\text{C}/\text{cm}^2$ dosage. The pattern encompassed diverse circle diameters, ranging from 500 to 300 nm. The separation distances between each VCSEP varied from $0.5 \mu\text{m}$ to $13 \mu\text{m}$.

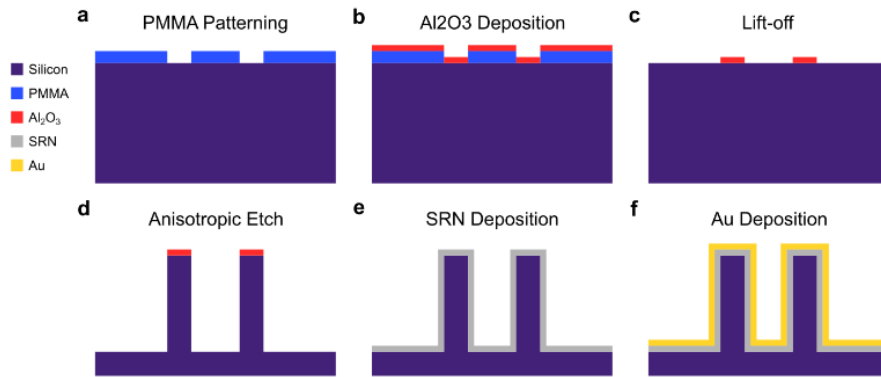


Fig. 3: The figure depicts the fabrication process of VCSEP and the steps involved in it. The process includes the following stages: (a) Spreading a 500 nm layer of PMMA using the spin-coating technique and transferring the pattern with E-beam, (b) Applying a layer of AL₂O₃ to act as a hard mask, (c) Removing the PMMA layer through lift-off, (d) Etching the pillars to a high aspect ratio and then removing the hard mask, (e) Coating the pillars with SRN, a nonlinear material, through PECVD method, and (f) Adding a thin layer of gold to the pillars.

A layer of aluminum oxide (Al₂O₃) was deposited onto the transferred pattern through sputtering, serving as a hard mask. Previous studies have demonstrated that Al₂O₃ exhibits a selectivity of 70,000:1 with respect to silicon[14], making it an optimal choice for sensitive fabrication processes such as VCSEP. A 5 nm Al₂O₃ layer was radio-frequency (RF) sputtered using the Denton Discovery 635 Sputter System. The chamber pressure was maintained at 3.18 mTorr, while the RF power was set at 400 W. The lift-off process was subsequently performed to remove the excess resistance.

Single-step deep reactive ion etching (SDRIE) was performed using the Oxford Plasma 100 system, employing a highly controlled etching recipe that ensured smooth walls and a high etching rate (200 nm/min) [8]. The etching of the VCSEP pillars was executed via a combination of sulfur hexafluoride (SF₆) and argon (Ar) plasma, supplemented with octafluorocyclobutane (C₄F₈) to protect the pillar sidewalls through the deposition of an organic polymer. The flow rates of SF₆ and C₄F₈ were maintained at 28 sccm and 52 sccm, respectively. The chamber pressure was held constant at 19 mTorr during the etching procedure. The reactive ion etching (RIE) and inductively coupled plasma (ICP) generated power were set at 9 W and 850 W, respectively[15].

The height of each pillar is 7.403 μm, exhibiting an aspect ratio of 15. The hard mask was removed via chemical etching, primarily utilizing buffered oxide etching (BOE) for 10 seconds at an etching rate of 1 nm/sec. To ensure complete removal of the polymer, the sample was cleaned with a piranha solution (a mixture of H₂SO₄:H₂O₂ in a 3:1 ratio) at a temperature of 80°C.

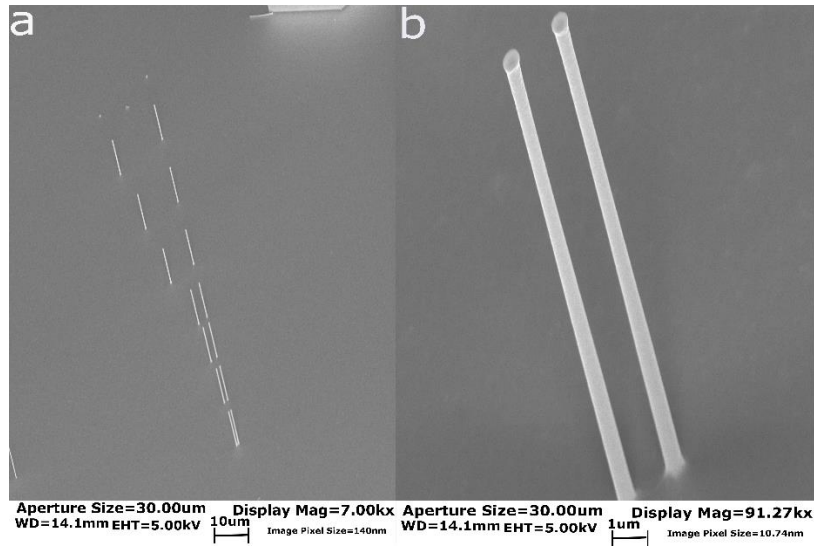


Fig. 4: SEM image of the VCSEP pillars after undergoing high aspect ratio etching. The image comprises two parts: (a) an SEM overview image of the VCSEP structure, which has a height of 7.403 μm , a width of 500 nm, and a separation gap ranging from 13 μm to 500 nm. (b) a zoomed-in SEM image showcases a pillar with an aspect ratio of 15 and a separation gap of 0.5 μm .

Subsequently, Plasma Enhanced Chemical Vapor Deposition (PECVD) was employed to coat the VCSEP pillars with SRN. The chamber pressure was maintained at 15 mTorr, and the radio-frequency (RF) power was set at 50 W. The SRN deposition involved a combination of silane (SiH_4) and ammonia (NH_3). SRN has been shown to demonstrate a third-order nonlinear susceptibility, $\chi^{(3)}$, as high as $(6 \pm 0.58) \times 10^{-19} \text{ m}^2/\text{V}$ [10]. In the final step, the pillars were coated with a thin layer of gold utilizing the Denton 635 Sputter System. During the sputtering process, the chamber pressure was constant at 2.88 mTorr. Direct current (DC) plasma power was set at 200 W for 55 seconds, giving a gold layer thickness of 60 nm and a rotated rate of 10 rpm.

5. CHARACTERIZATION:

The experimental setup shown in Figure 5 was used to characterize the VCSEP. The laser source (Agilent model 81980A) was connected to a single-mode fiber (SMF) and then collimated using a fiber collimator (FC) before transmitting light through the backend of the sample. A 0.5 NA microscope objective (50X Mitutoyo Plan Apo NIR) was used to gather the transmitted light, which was then imaged onto the camera through a magnification setup consisting of a series of lenses acting as two sequential telescopes with a total magnification of 100. The camera used featured a pixel size of 25 μm by 25 μm . Once the VCSEP device was located, the removable mirror was taken off (as shown in Figure 5) to collect the transmitted optical signal using a fiber collimated (FC) into a multimode fiber (MMF), which was then connected to a photodetector.

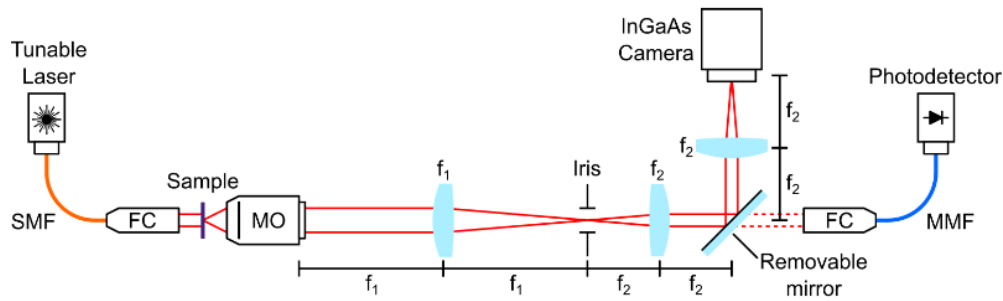


Fig. 5: Illustration of the experimental setup used to characterize the VCSEP. The setup includes a tunable laser connected to a Fiber Collimator (FC) through a Single Mode Fiber (SMF) to illuminate the backend of the sample. The transmitted light passes through a folded microscope setup composed of a 50X Mitutoyo Plan Apo NIR microscope objective (MO) and re-imaging lenses $f_1 = 40$ cm and $f_2 = 10$ cm. An iris is placed in an intermediate image plane to filter the system's field of view spatially. With the removable mirror in place, the final lens f_2 images collected light onto the IR camera, with a total system magnification of 100. Without the mirror, the light is directed to a photodetector using a fiber collimator and Multimode Fiber (MMF).

Characterizing a single VCSEP device poses challenges primarily due to its sub-micrometer diameter ($\sim 0.5\mu\text{m}$), making accurate structure identification difficult. To streamline the characterization process, large alignment marks are employed. This is illustrated in Figure 5, where the SEM images of the single VCSEP are positioned at the center of the alignment marks, as shown in Figure 5a. Additionally, Figure 5b displays an optical image captured from the IR camera at the end of the optical setup depicted in Figure 5.

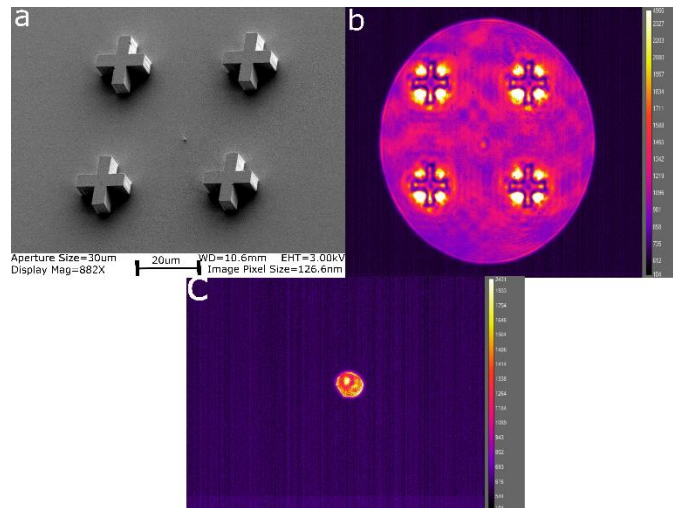


Fig. 6: The figure shows an improved VCSEP design incorporating an alignment mark to identify the device during imaging easily. The figure is composed of three parts: (a) an SEM image of the VCSEP device with an aspect ratio of 15 and height of $7.403\ \mu\text{m}$, which is surrounded by larger alignment marks to aid identification in an optical microscope, (b) an optical image captured by an IR camera that demonstrates optical transmission, and (c) an optical image captured by the IR camera that shows optical transmission after adding the iris to the image plane.

An iris was incorporated in the optical setup's first image plane to isolate a single VCSEP structure. This spatial filter limited any undesirable noise that could potentially impact device measurements. Fig.5c illustrates the optical image detected by the IR camera after employing the iris to confine the illuminated area as closely as possible to the VCSEP illuminated region with a diameter smaller than 1 mm.

6. RESULTS:

Each VCSEP device can be represented as a series of Fabry-Pérot (FP) resonators, with three distinct resonators, as depicted in the VCSEP cross-section presented in Figure 7. The first resonator comprises an air-silicon wafer-VCSEP arrangement, the second consists of an air-silicon wafer-air configuration, and the third resonator, which is our primary interest, features a silicon wafer-VCSEP-air composition.

Identifying the resonance response of each resonator is crucial for interpreting the results obtained from characterizing the single VCSEP device, as illustrated in Figure 7. The measured transmission corresponds to the collective response of the three resonators as the optical spot size surpasses the diameter of an individual VCSEP device. In this case, the aperture of the iris in the image plane imposes a constraint on the spot size, with the smallest aperture measuring 1mm. Additional information on the response of these resonators and their Free Spectral Ranges (FSRs) can be found in Figures 7b and c.

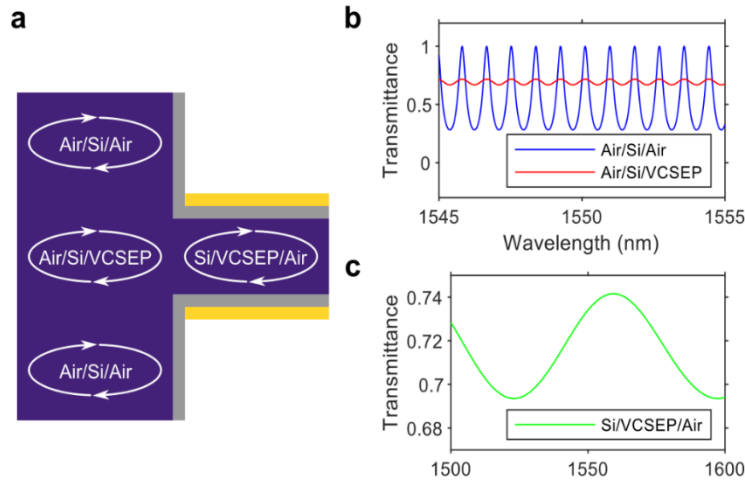


Fig. 7: A detailed cross-sectional theoretical model of the VCSEP device in three parts. Part (a) highlights the device's layered structure, comprising the highly doped silicon core, the nonlinear dielectric core (SRN), and the gold layer. Part (b) focuses on the theoretical optical response of the wafer resonator, which introduces high oscillation FP with a Free Spectral Range (FSR) of 1 nm and significantly impacts the device's overall performance. Part (c) demonstrates the combined effect of the wafer and the VCSEP device, offering insights into the device's optical behavior.

The theoretical response depicted in Figures 7b and 7c yields the following observations: The transmission spectra of the air-silicon wafer-air and the air-silicon wafer-VCSEP configurations exhibit a Free Spectral Range (FSR) of approximately 1 nm (as indicated in Figure 7b). Moreover, Figure 7c shows that the transmission spectra of the silicon-VCSEP-Air configuration have an FSR of approximately 70nm. An individual VCSEP with a fabricated height of 7.403 μm device was optically characterized utilizing a tunable laser source that could be tuned from 1520 nm to 1610 nm. The input power of the laser was set to 10 dBm. The optical responses of the device were investigated by focusing the laser light on two different areas of the substrate, one where no VCSEP device was present and the other where a device was present. The results of these optical measurements are shown in Figure 8.

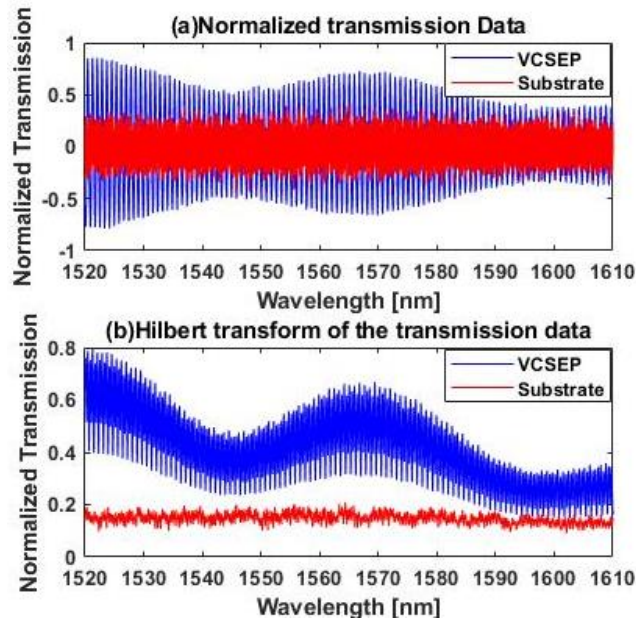


Fig. 8: The transmission spectrum of the VCSEP device and its corresponding silicon substrate can be represented by two distinct optical behaviors depicted by the blue and red curves, respectively: (a) shows the transmittance of the VCSEP device and substrate, normalized with respect to laser coupling efficiency; (b) The extraction of the Hilbert Transform of the transmission data, where the expected Free Spectral Range (FSR) of the VCSEP device is compared with the substrate response.

To observe the Fabry-Perot (FP) behavior of the VCSEP device and its characteristics, it is necessary to eliminate the effect of the power spectra of the light source and its impact on coupling efficiency. This effect is normalized by dividing each measured transmission of the VCSEP device by its corresponding value of the laser transmission. The normalized data is shown in Figure 8a, where the normalized transmitted data. This standard normalization procedure enables a more precise characterization of the device's optical behavior by removing the influence of the laser source's varying coupling efficiency. The normalization procedure is applied to both data sets obtained from the substrate, with and without the VCSEP device present. The blue and red curves in the respective represent these data sets.

Next, a Hilbert Transform was applied to the transmission data obtained from the substrate and the VCSEP device to eliminate the rapid oscillations due to wafer resonance. This procedure resulted in a curve corresponding to the transmission of a single VCSEP device, represented by the blue line in Figure 8b. The Hilbert transform of the transmission data obtained from the substrate showed no slow oscillations. No VCSEP structure was present in those measurements. The optical response of an individual VCSEP device with an aspect ratio of 15 was estimated to have a Free Spectral Range (FSR) of 53.28 ± 2.5 nm, with transmission variation around 3 dB.

Figure 9 illustrates the electro-optic response of the VCSEP device. Figure 9a shows the wavelength shift as a function of applied voltage. Figure 9b illustrates the resulting changes in the effective index with the best fitting results as a function of applied voltage.

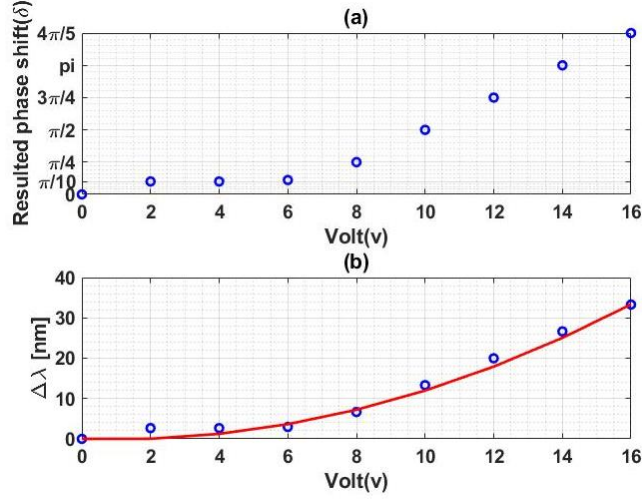


Fig. 9: The figure illustrates the electro-optic response of the VCSEP device, presenting two parts. Part (a) demonstrates the resulting phase shift (δ) in rad representation as a function of the applied electrical field Part (b) shows the wavelength shift in nanometer versus the applied electrical field.

This response quantitatively measures the device's phase-shifting capabilities, highlighting the relationship between the applied electrical field and the resulting phase shift. It is observed that a π phase shift can be introduced by applying a voltage equal to 14V, which equals a shift in the wavelength to 53.28nm. Moreover, we can estimate the value of $V\pi L$, equal to 0.0075 V-cm, and insertion loss equal to 69dB. The VCSEP device demonstrates a maximum phase shift of 1.25π under an applied electric field intensity of 16V. Furthermore, the variation in the effective index can be estimated using the equation below:

$$\frac{\Delta n}{n} \frac{L_{mod}}{L_{total}} = \frac{\Delta \lambda}{\lambda} \quad (1)$$

Using the developed COMSOL model of the VCSEP device, we can extrapolate the mode length (L_{mod}) and total interaction length (L_{total}) to estimate the variation in the effective index as a function of applied voltage. The best fitting is achieved by using the following equation:

$$\Delta n = \frac{3\Gamma_{SRN}}{2} \times \frac{\chi^3}{n_g} E^2 + \frac{3\Gamma_{SRN}}{2} \times \frac{\chi^2}{n_g} E \quad (2)$$

Where Γ_{SRN} is the mode overlap with SRN and E is the applied electric field across the VCSEP.

7. DISCUSSION

The results presented in this study demonstrate the effectiveness of the VCSEP device for optical applications. The device was characterized using a tunable laser, and the responses of its three distinct Fabry-Perot resonators were analyzed. The measurements yielded an FSR of 1 nm for the air-silicon wafer-air and air-silicon wafer-VCSEP configurations and an FSR of 70 nm for the silicon-VCSEP-Air configuration.

A normalization procedure was applied to the data obtained from the substrate to obtain a more accurate characterization of the device's optical behavior. A Hilbert Transform eliminated the rapid oscillations due to wafer resonance. The resulting analysis provided an estimated FSR of 53.25 ± 2.5 nm for the measured VCSEP Fabry-Perot, within the expected range, despite being off by approximately 16.75 nm from the module-suggested FSR of 70 nm.

According to simulation results, the VCSEP configuration was estimated to have an insertion loss of 2500 dB. In theory, experimental verification of this value could be accomplished using the finesse of the resonator. However, accurately estimating the exact insertion loss of the VCSEP device is challenging due to its small size. The device is estimated to be less than 1 μm in dimension, and when considering the total magnification of the setup (100x), the imaged device appears to be less than 100 μm . This can be observed in Figure 6c, which indicates that the VCSEP occupies less than 50% of the image. Moreover, the presence of multiple resonances from the presence of multiple interfaces in the fabricated sample results in a complex experimental spectrum that renders this extraction impossible.

Considering the total experimental structure (not only the VCSEP but the substrate wafer, etc.) reveals several additional sources of optical loss. The total insertion loss is 69 dB and may be attributed to various factors, including the high absorption coefficient of n-doped silicon with a doping concentration of $4.5 \times 10^{18} \text{ cm}^{-3}$. The loss will also occur due to absorption at the gold layer and multiple reflections between the gold, SRN, and silicon layers. Most of this loss is due to the doped silicon backing layer, which could be largely ameliorated using a thinner layer. This layer cannot be removed completely, as it is necessary as an electrical contact at the base of the VCSEP.

The difference between the best fitting result from the Δn can be attributed to the non-ohmic contact. Non-ohmic contacts exhibit nonlinear current-voltage characteristics due to the high barrier height at the metal-semiconductor junction in non-ohmic contacts, which inhibits the flow of charge carriers across the junction. As a result, the voltage required to vary the effective index is higher.

Furthermore, the VCSEP device was found to have $V_{\pi}L$ equal to 0.0075 V-cm, equivalent to 0.075 V-mm. This value is comparable to state-of-the-art P-i-n devices with a $V_{\pi}L$ of 0.036 V-mm[16]. In contrast, the typical Si modulator has a $V_{\pi}L$ of 4 V-cm[17]. The high $V_{\pi}L$ of our device is made possible by the combination of its vertical cylindrical topology, hybrid plasmonic guided modes, relatively high r_{33} of SRN, and Fabry-Pérot resonance in the vertical direction of propagation. Furthermore, the device's performance can be further improved by utilizing a nonlinear material such as silicon-rich nitride (SRN), which has been shown to demonstrate a third-order nonlinear susceptibility, χ^3 , as high as $(6 \pm 0.58) \times 10^{-19} \text{ m}^2/\text{V}$ [10].

One way to enhance the design is to use thermal annealing to reduce the Schottky barrier and establish an ohmic contact by forming a silicide layer at the metal-silicon interface. The choice of metal for the contact can vary depending on the type and concentration of doping. For n-doped silicon, such as in our case, platinum (Pt) is commonly used as the contact metal in the literature[18]. The sample can then be thermally annealed using Rapid Thermal Annealing (RTA) at a temperature range of 400°C to 500°C for 5 to 10 minutes, depending on the doping concentration. Higher doping concentrations require lower annealing temperatures and times [18].

This detailed analysis of the transmission spectrum of the VCSEP device and its comparison with the silicon substrate provides valuable insights into the device's optical properties and potential applications in photonic systems.

8. CONCLUSION:

This manuscript introduces an innovative approach to developing compact phase shifters for LIDAR applications using hybrid resonance structures in a cylindrical topology. The study showcases fabricated devices with high aspect ratios and small footprints, enabling dense integration of VCSEPs. By applying normalization procedures and analysis techniques, the study accurately characterizes the optical behavior of the VCSEP device. Despite uncertainties in measuring insertion loss, the study demonstrates the promising potential for optical applications, with the VCSEP device having a $V_{\pi}L$ 0.075 V-mm comparable performance to state-of-the-art devices. The findings provide valuable insights into the optical properties of the VCSEP device and suggest future research directions in utilizing nonlinear materials for

enhanced performance. Overall, the study paves the way for developing high-performance, compact electro-optic phase shifters suitable for various LIDAR applications.

Funding. Defense Advanced Research Projects Agency (DSO NAC Programs); Office of Naval Research; National Science Foundation (ECCS-180789, NSF ECCS-190184, NSF ECCS-2023730); Army Research Office; San Diego Nanotechnology Infrastructure (SDNI) supported by the NSF National Nanotechnology Coordinated Infrastructure (ECCS-2025752); Quantum Materials for Energy Efficient Neuromorphic Computing-an Energy Frontier Research Center funded by the US Department of Energy (DOE) Office of Science; Basic Energy Sciences (DE-SC0019273); LEED: A Lightwave Energy-Efficient Datacenter funded by the Advanced Research Projects Agency-Energy; Cymer Corporation.

Acknowledgments. The author, Dhaifallah Almutairi, would like to acknowledge the support provided by King Abdulaziz City for Science and Technology (KACST) during their study. The authors also extend their appreciation to the nano3 cleanroom staff at UCSD and Dr. Maribel Montero for their valuable assistance in fabricating the samples. In addition, the author would like to express gratitude to the CPTF lab for granting access to their facilities for characterizing the demonstrated device.

Disclosures. The authors declare no conflicts of interest.

Data availability. Data underlying the results presented in this paper are not publicly available but may be obtained from the authors upon reasonable request.

REFERENCES:

1. P.-C. Kuo, S.-I. Kuo, J.-W. Wang, Y.-H. Jian, Z. Ahmad, P.-H. Fu, Y.-C. Chang, J.-W. Shi, D.-W. Huang, Y. Liu, C.-H. Yeh, and C.-W. Chow, *Actively Steerable Integrated Optical Phased Array (OPA) for Optical Wireless Communication (OWC); Actively Steerable Integrated Optical Phased Array (OPA) for Optical Wireless Communication (OWC)* (2022).
2. C. V. Poulton, M. J. Byrd, P. Russo, E. Timurdogan, M. Khandaker, Di. Vermeulen, and M. R. Watts, "Long-Range LiDAR and Free-Space Data Communication with High-Performance Optical Phased Arrays," *IEEE Journal of Selected Topics in Quantum Electronics* **25**(5), (2019).
3. shengping liu, Y. Tian, Y. Lu, and J. Feng, "Comparison of thermo-optic phase-shifters implemented on CUMEC silicon photonics platform," in (SPIE-Intl Soc Optical Eng, 2021), p. 328.
4. A. F. Fercher, W. Drexler, C. K. Hitzenberger, and T. Lasser, *Optical Coherence Tomography-Principles and Applications* (2003), **66**.
5. *Review on LiDAR Technology Srushti Neoge · Ninad Mehendale* (n.d.).
6. N. Li, C. P. Ho, J. Xue, L. W. Lim, G. Chen, Y. H. Fu, and L. Y. T. Lee, "A Progress Review on Solid-State LiDAR and Nanophotonics-Based LiDAR Sensors," *Laser Photon Rev* **16**(11), (2022).

7. J. Sun, E. Timurdogan, A. Yaacobi, E. S. Hosseini, and M. R. Watts, "Large-scale nanophotonic phased array," *Nature* **493**(7431), 195–199 (2013).
8. B. Janjan, D. Fathi, M. Miri, and M. Ghaffari-Miab, "Ultra-wideband high-speed Mach–Zehnder switch based on hybrid plasmonic waveguides," *Appl Opt* **56**(6), 1717 (2017).
9. R. F. Oulton, V. J. Sorger, D. A. Genov, D. F. P. Pile, and X. Zhang, "A hybrid plasmonic waveguide for subwavelength confinement and long-range propagation," *Nat Photonics* **2**(8), 496–500 (2008).
10. A. Friedman, H. Nejadriaqi, R. Sharma, and Y. Fainman, "Demonstration of the DC-Kerr effect in silicon-rich nitride," *Opt Lett* **46**(17), 4236 (2021).
11. S. Habermehl, R. T. Apodaca, and R. J. Kaplar, "On dielectric breakdown in silicon-rich silicon nitride thin films," *Appl Phys Lett* **94**(1), (2009).
12. D. K. T. Ng, Q. Wang, T. Wang, S. K. Ng, Y. T. Toh, K. P. Lim, Y. Yang, and D. T. H. Tan, "Exploring High Refractive Index Silicon-Rich Nitride Films by Low-Temperature Inductively Coupled Plasma Chemical Vapor Deposition and Applications for Integrated Waveguides," *ACS Appl Mater Interfaces* **7**(39), 21884–21889 (2015).
13. H.-H. Lin, R. Sharma, M.-H. Yang, M. W. Puckett, C. D. Wurm, F. Vallini, and Y. Fainman, *Enhanced Effective Second-Order Nonlinearities in Si-Rich SiN_x Thin Films* (2017).
14. K. Grigoras, L. Sainiemi, J. Tiilikainen, A. Säynätjoki, V. M. Airaksinen, and S. Franssila, "Application of ultra-thin aluminum oxide etch mask made by atomic layer deposition technique," *J Phys Conf Ser* **61**(1), 369–373 (2007).
15. Y.-J. Hung, S.-L. Lee, L. A. Coldren, H. Benisty, J. Lourtioz, A. Chelnokov, S. Combrie, X. Checoury, D. Ko, J. R. Tumbleston, L. Zhang, S. Williams, J. M. DeSimone, R. Lopez, E. T. Samulski, J. Ruebusch, A. A. Rathore, R. Kapadia, O. Ergen, P. W. Leu, A. Javey, J. Zhu, Z. Yu, G. F. Burkhard, C. Hsu, S. T. Connor, Y. Xu, Q. Wang, M. McGehee, S. Fan, and Y. Cui, *5. Z. Fan, DChallenges and Prospects of Nanopillar-Based Solar Cells* (2006), **120**(1).
16. H. C. Nguyen, S. Hashimoto, M. Shinkawa, T. Baba, D. J. Thomson, F. Y. Gardes, J. M. Fedeli, S. Zlatanovic, H. Youfang, B. P. P Kuo, E. Myslivets, N. Alic, M. Ziebell, D. Marris-Morini, G. Rasigade, J. M. Fédéli, P. Crozat, E. Cassan, D. Bouville, L. Vivien, H. Yu, M. Pantouvaki, J. Van Campenhout, D.

Korn, K. Komorowska, P. Dumon, Y. L. Li, P. Verheyen, P. Absil, L. Alloatti, D. Hillerkuss, J. Leuthold, and R. Baets, *Ultra-Compact, Low RF Power, 10 Gb/s Silicon Mach-Zehnder Modulator* (2010), **4**(8).

17. H. C. Nguyen, S. Hashimoto, M. Shinkawa, and T. Baba, "Compact and fast photonic crystal  silicon optical modulators," *Opt. Express* **20**(20), 22465–22474 (2012).
18. G. P. Lousberg, H. Y. Yu, B. Froment, E. Augendre, A. De Keersgieter, A. Lauwers, M. F. Li, P. Absil, M. Jurczak, and S. Biesemans, "Schottky-barrier height lowering by an increase of the substrate doping in PtSi Schottky barrier source/drain FETs," *IEEE Electron Device Letters* **28**(2), 123–125 (2007).