

# SMALL JUMP WITH NEGATION-UTM TRAMPOLINE

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## 1. INTRODUCTION

This paper divide some complexity class by using fixpoint and fixpointless area of Decidable Universal Turing Machine (UTM). Decidable Deterministic Turing Machine (DTM) have fixpointless combinator that add no extra resources (like Negation), but UTM makes some fixpoint in the combinator. This means that we can jump out of the fixpointless combinator system by making more complex problem from diagonalisation argument of UTM.

As a concrete example, we proof L is not P . We can make Polynomial time UTM that emulate all Logarithm space DTM (LDTM). LDTM set close under Negation, therefore UTM does not close under LDTM set. (We can proof this theorem like halting problem and time/space hierarchy theorem, and also we can extend this proof to divide time/space limited DTM set.) In the same way, we proof P is not NP. These are new hierarchy that use UTM and Negation.

## 2. L IS NOT P

**Definition 1.** “DTM” is defined as Decidable Deterministic Turing Machine set. “LDTM” is defined as logarithmic space DTM. “pDTM” is defined as polynomial time DTM. “ $\bigcirc$ DTM” is defined as DTM that some resource (time, space) limited.

“UTM” is defined as Universal Turing Machine set. “ $UTM(C)$ ” is defined as minimum UTM that can emulate all  $M \in C$ .  $\langle M \rangle$  is defined as code number of a  $M \in DTM$  that  $U \in UTM$  emulate. That is,  $\forall w [U(\langle M \rangle, w) = M(w)]$  and  $U(\langle M \rangle) = M$ .

“Negate( $C$ )” is defined as minimum Negation system that include  $C$ . That is,  $\forall C [(C \subset Negate(C)) \wedge (\forall c \in Negate(C) [\neg c \in Negate(C)])]$ .

**Theorem 2.**  $\forall r \in \bigcirc DTM (\neg r \in \bigcirc DTM)$

*Proof.* It is trivial from DTM structure.

If DTM is

$$M = (Q, \Sigma, \Gamma, \delta, q_0, q_1, q_2)$$

then this dual machine

$$\bar{M} = (Q, \Sigma, \Gamma, \delta, q_0, q_2, q_1)$$

compute  $\neg M$  without extra resources.

Therefore negation of  $\bigcirc DTM$  is also in  $\bigcirc DTM$ . □

**Theorem 3.**  $\exists U \in UTM (LDTM) [U \in pDTM]$

*Proof.* It is trivial because some  $U' \in UTM$  can emulate all LDTM in polynomial time. Therefore, we can make  $U \in pDTM$  by limiting at polynomial time (if  $U'$  compute over polynomial time,  $U$  reject these input). □

**Theorem 4.**  $L \subsetneq P$

*Proof.* We can proof this theorem like halting problem and time/space hierarchy theorem.

Because of

$$\forall U \in UTM(LDTM), M \in LDTM [U(\langle M \rangle) = M] \quad 1$$

all  $M \in LDTM$  have index  $\langle M \rangle$ . Therefore we can make  $H$  which is diagonalization of  $U$ .

$$H(\langle M \rangle) = U(\langle M \rangle, \langle M \rangle)$$

	$\langle M_0 \rangle$	$\langle M_1 \rangle$	$\langle M_2 \rangle$	$\langle M_3 \rangle$	$\dots$
$M_0 = \{$	$\overline{\perp}$	$\top$	$\perp$	$\top$	$\dots$
$M_1 = \{$	$\perp$	$\underline{\perp}$	$\top$	$\perp$	$\dots$
$M_2 = \{$	$\perp$	$\perp$	$\underline{\perp}$	$\top$	$\dots$
$M_3 = \{$	$\top$	$\top$	$\perp$	$\underline{\perp}$	$\dots$
$\vdots$	$\vdots$	$\vdots$	$\vdots$	$\vdots$	
$H = \{$	$\top$	$\perp$	$\perp$	$\perp$	$\dots$

$H \in pDTM$  because  $U \in pDTM$  2 and  $H$  input size is at least half of  $U$ .

Mentioned above 2,

$$\forall r \in LDTM (\neg r \in LDTM)$$

we can make  $G$  which is Negation of diagonalization.

$$G(\langle M \rangle) = \neg H(\langle M \rangle) = \neg U(\langle M \rangle, \langle M \rangle)$$

	$\langle M_0 \rangle$	$\langle M_1 \rangle$	$\langle M_2 \rangle$	$\langle M_3 \rangle$	$\dots$
$M_0 = \{$	$\overline{\perp}$	$\top$	$\perp$	$\top$	$\dots$
$M_1 = \{$	$\perp$	$\underline{\perp}$	$\top$	$\perp$	$\dots$
$M_2 = \{$	$\perp$	$\perp$	$\underline{\perp}$	$\top$	$\dots$
$M_3 = \{$	$\top$	$\top$	$\perp$	$\underline{\perp}$	$\dots$
$\vdots$	$\vdots$	$\vdots$	$\vdots$	$\vdots$	
$H = \{$	$\top$	$\perp$	$\perp$	$\perp$	$\dots$
$G = \{$	$\perp$	$\top$	$\top$	$\top$	$\dots$

$G \notin LDTM$  because  $\forall M \in LDTM [G(\langle M \rangle) \neq M(\langle M \rangle)]$ . On the other hand,  $G \in pDTM$  because  $H \in pDTM$ .

Therefore,  $G \in pDTM (G \notin LDTM)$  and  $L \subsetneq P$ . □

We can expand above result to general DTM.

**Theorem 5.**  $\forall CC \subset DTM [Negate(UTM(Negate(CC))) \not\subseteq Negate(CC)]$

*Proof.* We omit the proof because this proof is same as previous. □

**Corollary 6.**  $Negate(UTM(\circ DTM)) \not\subseteq \circ DTM$

### 3. P IS NOT NP

**Theorem 7.**  $\exists U \in UTM(pDTM) [U \in pNTM]$

*Proof.* We can make some oracle TM which oracle emulate transition function.

$$np^p \mid np \in NP, p \in P$$

$$p(\langle t \rangle, w) = t(w)$$

$t \in P$ : transition function

$\langle t \rangle$ : code number of transition function  $t$

$w$ :  $t$ 's input (state and symbol)

$p(\langle t \rangle, w)$  accept if and only if  $t(w)$  accept and output  $t(w)$ .

Oracle TM  $np^p \in pNTM$  and  $np^p$  can emulate all  $pDTM$ . Therefore  $np^p \in UTM(pDTM)$ .  $\square$

*Note 8.*  $np^p$  can change transition function more flexible than  $pDTM$  in less time. In fact,  $np^p$  can increase transition function with logarithm time (by computing these transition functions in parallel). Therefore,  $np^p$  have more chance to compute more complex problems.

**Theorem 9.**  $P \subsetneq NP$

*Proof.* (Proof by contradiction.) Assume to the contrary that  $P = NP$ .

$P = NP$  means that  $P = NP = coNP = PH$ , therefore  $NP$  close under Negation.

In the same way as mentioned above 4, we get  $P \subsetneq NP$ . This result contradicting assumption  $P = NP$ .

Therefore  $P \subsetneq NP$ .  $\square$

**Corollary 10.**  $P \subsetneq coNP$

#### 4. TRAMPOLINE HIERARCHY BETWEEN NEGATION AND UTM

This result shows that we can jump over border of asymptotic analysis by using Negation (fixpointless combinator) and UTM (fixpoint creator). Therefore, combination of UTM and Negation make new complexity class. That is, there are some Hierarchy of UTM and Negation.

#### REFERENCES

- [1] Michael Sipser, (translation) Kazuo OHTA, Keisuke TANAKA, Masayuki ABE, Hiroki UEDA, Atsushi FUJIOKA, Osamu WATANABE, "Introduction to the Theory of COMPUTATION Second Edition (Japanese version)", 2008